

In re Application of:)))		
Bartz et al.,))	Examiner:	Whitmore	
Stacy					
Serial No.: 09/989,808)) Art	Unit: 2825		
Filing Date: November 19, 2001))			
FOR: AUTOMATIC GENERATION OF) APPLICATION PROGRAM INTERFACES, SOURCE CODE, INTERRUPTS, AND DATASHEETS FOR MICROCONTROLLER PROGRAMMING))				
INOGRALITING)			(

DECLARATION UNDER 37 C.F.R. §1.132 TRAVERSING GROUNDS OF REJECTION UNDER 35 U.S.C. § 102(a)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

I, Douglas H. Anderson, do hereby declare and say:

My home address is 18427 Olympic View Dr., Edmonds, WA 98020.

On November 16, 2001 I signed a declaration stating that I believe that I am an original and joint inventor of the subject matter that is claimed in U.S. Patent Application No. 09/989,808, entitled, "Automatic Generation Of Application Program Interfaces, Source Code, Interrupts, And Datasheets For Microcontroller Programming"

(hereinafter, the present Patent Application). The present Patent Application was filed in the United States Patent and Trademark Office on November 19, 2001. The declaration stating that I believe that I am an original and joint inventor of the subject matter claimed in the present Patent Application was filed in the United States Patent and Trademark Office, along with the present Patent Application.

I have studied the entire specification, drawings, and claims of the present Patent Application. I understand the language of the claims and the scope thereof. I understand the invention as claimed.

As an employee of Cypress Microsystems, a subsidiary of Cypress Semiconductor, subject matter that I originate may be incorporated into Cypress documents, some of which may become public.

I have reviewed Revision 1.0 of the document "PSoC Designer:
Integrated Development Environment: Getting Started 12-Minute
Tutorial" (hereinafter, IDE Tutorial). It is my belief that, to the
extent that subject matter exists in the document "IDE Tutorial"
that may anticipate claims of the present Patent Application, such
information was derived from the work of myself and my co-inventors
of the present Patent Application: Manfred Bartz, Marat Zhaksilikov,
Steve Roe, Kenneth Y. Ogami, and Matthew A. Pleis.

I have reviewed "PSoC™ Technology Completely Changes 8-Bit MCU

System Design!" (hereinafter, PSoC™ Technology). It is my belief

that, to the extent that subject matter exists in the document

"PSoC™ Technology" that may anticipate claims of the present Patent

Application, such information was derived from the work of myself

and my co-inventors of the present Patent Application: Manfred

Bartz, Marat Zhaksilikov, Steve Roe, Kenneth Y. Ogami, and Matthew

A. Pleis.

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Respectfully submitted,

Date: <u>67/62</u>, 2005

Full Name:

Douglas H. Anderson



In re Application of:	,)		
Bartz et al.,	,) Examiner: Whitmore,		
Stacy	1 =	•		
Serial No.: 09/989,808)) Art Unit: 2825		
Filing Date: November 19, 2001))		
FOR: AUTOMATIC GENERATION OF) APPLICATION PROGRAM INTERFACES, SOURCE CODE, INTERRUPTS, AND)	•		
DATASHEETS FOR MICROCONTROLLER PROGRAMMING)			
)			

DECLARATION UNDER 37 C.F.R. §1.132 TRAVERSING GROUNDS OF REJECTION UNDER 35 U.S.C. § 102(a)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

I, Kenneth Y. Ogami, do hereby declare and say:

My home address is 14232 93rd Ave., NE, Bothell, WA 98011.

On November 16, 2001 I signed a declaration stating that I believe that I am an original and joint inventor of the subject matter that is claimed in U.S. Patent Application No. 09/989,808, entitled, "Automatic Generation Of Application Program Interfaces, Source Code, Interrupts, And Datasheets For Microcontroller Programming"

(hereinafter, the present Patent Application). The present Patent Application was filed in the United States Patent and Trademark Office on November 19, 2001. The declaration stating that I believe that I am an original and joint inventor of the subject matter claimed in the present Patent Application was filed in the United States Patent and Trademark Office, along with the present Patent Application.

I have studied the entire specification, drawings, and claims of the present Patent Application. I understand the language of the claims and the scope thereof. I understand the invention as claimed.

As an employee of Cypress Microsystems, a subsidiary of Cypress Semiconductor, subject matter that I originate may be incorporated into Cypress documents, some of which may become public.

I have reviewed Revision 1.0 of the document "PSoC Designer: Integrated Development Environment: Getting Started 12-Minute Tutorial" (hereinafter, IDE Tutorial). It is my belief that, to the extent that subject matter exists in the document "IDE Tutorial" that may anticipate claims of the present Patent Application, such information was derived from the work of myself and my co-inventors of the present Patent Application: Manfred Bartz, Marat Zhaksilikov, Steve Roe, Matthew A. Pleis, and Douglas H. Anderson.

I have reviewed "PSoC™ Technology Completely Changes 8-Bit MCU System Design!" (hereinafter, PSoC™ Technology). It is my belief that, to the extent that subject matter exists in the document "PSoC™ Technology" that may anticipate claims of the present Patent Application, such information was derived from the work of myself and my co-inventors of the present Patent Application: Manfred Bartz, Marat Zhaksilikov, Steve Roe, Matthew A. Pleis, and Douglas H. Anderson.

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Respectfully submitted,

Date: JUNC 50, 2005

Full Name:

Kenneth Y. Ogami



,)		· ·
))	Examiner:	Whitmore,
\			
)) Art	Unit: 2825	
))		
))))	·		•
)))))))))))) Art)))))))))) Examiner:)) Art Unit: 2825)))))

DECLARATION UNDER 37 C.F.R. §1.132 TRAVERSING GROUNDS OF REJECTION UNDER 35 U.S.C. § 102(a)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

I, Matthew A. Pleis, do hereby declare and say:

My home address is 11733 E. LK. Joy Dr., NE, Carnation, WA 98014.

On November 16, 2001 I signed a declaration stating that I believe that I am an original and joint inventor of the subject matter that is claimed in U.S. Patent Application No. 09/989,808, entitled, "Automatic Generation Of Application Program Interfaces, Source

Code, Interrupts, And Datasheets For Microcontroller Programming" (hereinafter, the present Patent Application). The present Patent Application was filed in the United States Patent and Trademark Office on November 19, 2001. The declaration stating that I believe that I am an original and joint inventor of the subject matter claimed in the present Patent Application was filed in the United States Patent and Trademark Office, along with the present Patent Application.

I have studied the entire specification, drawings, and claims of the present Patent Application. I understand the language of the claims and the scope thereof. I understand the invention as claimed.

As an employee of Cypress Microsystems, a subsidiary of Cypress Semiconductor, subject matter that I originate may be incorporated into Cypress documents, some of which may become public.

I have reviewed Revision 1.0 of the document "PSoC Designer:
Integrated Development Environment: Getting Started 12-Minute
Tutorial" (hereinafter, IDE Tutorial). It is my belief that, to the
extent that subject matter exists in the document "IDE Tutorial"
that may anticipate claims of the present Patent Application, such
information was derived from the work of myself and my co-inventors
of the present Patent Application: Manfred Bartz, Marat Zhaksilikov,
Steve Roe, Kenneth Y. Ogami, and Douglas H. Anderson.

I have reviewed "PSoC™ Technology Completely Changes 8-Bit MCU System Design!" (hereinafter, PSoC™ Technology). It is my belief that, to the extent that subject matter exists in the document "PSoC™ Technology" that may anticipate claims of the present Patent Application, such information was derived from the work of myself and my co-inventors of the present Patent Application: Manfred Bartz, Marat Zhaksilikov, Steve Roe, Kenneth Y. Ogami, and Douglas H. Anderson.

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Respectfully submitted,

Date: July 7, 2005

Full Name:

Matthew A. Pleis



In re Application of:)
Bartz et al.,	Examiner: Whitmore, Stacy
Serial No.: 09/989,808)) Art Unit: 2825
Filing Date: November 19, 2001)
FOR: AUTOMATIC GENERATION OF APPLICATION PROGRAM INTERFACES, SOURCE CODE, INTERRUPTS, AND DATASHEETS FOR MICROCONTROLLER PROGRAMMING))))
	_)

DECLARATION UNDER 37 C.F.R. §1.132 TRAVERSING GROUNDS OF REJECTION UNDER 35 U.S.C. § 102(a)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

I, Manfred Bartz, do hereby declare and say:

My home address is 10303 172nd Ave., SE, Snohomish, WA 98290.

On November 16, 2001 I signed a declaration stating that I believe that I am an original and joint inventor of the subject matter that is claimed in U.S. Patent Application No. 09/989,808, entitled, "Automatic Generation Of Application Program Interfaces, Source Code, Interrupts, And Datasheets For Microcontroller

CYPR-CD01169M Art Group 2825 Serial No. 09/989,808 Examiner: Whitmore, Stacy Programming" (hereinafter, the present Patent Application). The present Patent Application was filed in the United States Patent and Trademark Office on November 19, 2001. The declaration stating that I believe that I am an original and joint inventor of the subject matter claimed in the present Patent Application was filed in the United States Patent and Trademark Office, along with the present Patent Application.

I have studied the entire specification, drawings, and claims of the present Patent Application. I understand the language of the claims and the scope thereof. I understand the invention as claimed.

When I was an employee of Cypress Microsystems, a subsidiary of Cypress Semiconductor, subject matter that I originated was subject to be incorporated into Cypress documents, some of which may have become public.

I have reviewed Revision 1.0 of the document "PSoC Designer: Integrated Development Environment: Getting Started 12-Minute Tutorial" (hereinafter, IDE Tutorial). It is my belief that, to the extent that subject matter exists in the document "IDE Tutorial" that may anticipate claims of the present Patent Application, such information was derived from the work of myself and my co-inventors of the present Patent Application: Matthew A. Pleis, Marat Zhaksilikov, Steve Roe, Kenneth Y. Ogami, and Douglas H. Anderson.

Serial No. 09/989,808 Examiner: Whitmore, Stacy I have reviewed "PSoCTM Technology Completely Changes 8-Bit MCU System

Design!" (hereinafter, PSoCTM Technology). It is my belief that, to the extent that

subject matter exists in the document "PSoCTM Technology" that may anticipate

claims of the present Patent Application, such information was derived from the

work of myself and my co-inventors of the present Patent Application: Matthew A.

Pleis, Marat Zhaksilikov, Steve Roe, Kenneth Y. Ogami, and Douglas H. Anderson.

I further declare that all statements made herein of my own knowledge are true

and that all statements made on information and belief are believed to be true;

and further that these statements were made with the knowledge that willful

false statements and the like so made are punishable by fine or imprisonment, or

both, under Section 1001 of Title 18 of the United States Code, and that such

willful false statements may jeopardize the validity of the application or any

patent issued thereon.

Date: 7/105, 2005

Respectfully submitted,

Signature

Full Name: Manfred Bartz

CYPR-CD01169M Art Group 2825 Serial No. 09/989,808 Examiner: Whitmore, Stacy

-3



In re Application of:)		
Bartz et al.,)	Examiner:	Whitmore,
Stacy	1		
Serial No.: 09/989,808))) Art U	Jnit: 2825	
Filing Date: November 19, 2001)		
FOR: AUTOMATIC GENERATION OF) APPLICATION PROGRAM INTERFACES, SOURCE CODE, INTERRUPTS, AND DATASHEETS FOR MICROCONTROLLER PROGRAMMING))))		
	<u></u> '		

DECLARATION UNDER 37 C.F.R. §1.132 TRAVERSING GROUNDS OF REJECTION UNDER 35 U.S.C. § 102(a)

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

I, Steve Roe, do hereby declare and say:

My home address is 18208 NE 194th St., Woodinville, WA 90872.

On November 16, 2001 I signed a declaration stating that I believe that I am an original and joint inventor of the subject matter that is claimed in U.S. Patent Application No. 09/989,808, entitled, "Automatic Generation Of Application Program Interfaces, Source Code, Interrupts, And Datasheets For Microcontroller Programming"

(hereinafter, the present Patent Application). The present Patent Application was filed in the United States Patent and Trademark Office on November 19, 2001. The declaration stating that I believe that I am an original and joint inventor of the subject matter claimed in the present Patent Application was filed in the United States Patent and Trademark Office, along with the present Patent Application.

I have studied the entire specification, drawings, and claims of the present Patent Application. I understand the language of the claims and the scope thereof. I understand the invention as claimed.

As an employee of Cypress Microsystems, a subsidiary of Cypress Semiconductor, subject matter that I originate may be incorporated into Cypress documents, some of which may become public.

I have reviewed Revision 1.0 of the document "PSoC Designer:
Integrated Development Environment: Getting Started 12-Minute
Tutorial" (hereinafter, IDE Tutorial). It is my belief that, to the
extent that subject matter exists in the document "IDE Tutorial"
that may anticipate claims of the present Patent Application, such
information was derived from the work of myself and my co-inventors
of the present Patent Application: Manfred Bartz, Marat Zhaksilikov,
Kenneth Y. Ogami, Matthew A. Pleis, and Douglas H. Anderson.

I have reviewed "PSoCTM Technology Completely Changes 8-Bit MCU System Design!" (hereinafter, PSoCTM Technology). It is my belief that, to the extent that subject matter exists in the document "PSoCTM Technology" that may anticipate claims of the present Patent Application, such information was derived from the work of myself and my co-inventors of the present Patent Application: Manfred Bartz, Marat Zhaksilikov, Kenneth Y. Ogami, Matthew A. Pleis, and Douglas H. Anderson.

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Respectfully submitted,

Date: 2005

Signature

Full Name:

Steve Roe